

Docket No.: MICRON.104DV1

Date: February 22, 2005

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REQUEST FOR CONTINUED EXAMINATION (RCE) UNDER 37 C.F.R. § 1.114

Applicant:

TRIVEDI, Jigish D.

Application Number:

10/631,921

Filing Date:

July 31, 2003

Title:

TRANSISTOR GATE AND LOCAL

INTERCONNECT

Examiner Name:

POMPEY, Ron E.

Group Art Unit:

2812

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first-class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Mail Stop RCE, Alexandria, VA 22313-1450, on

February 22, 2005

(Date)

James W. Ausley, Reg. No. 49,76

Commissioner for Patents P.O. Box 1450 Mail Stop RCE Alexandria, VA 22313-1450

This is a Request for Continued Examination (RCE) under 37 C.F.R. § 1.114 of the above-identified application.

- 1. Submission Required under 37 C.F.R. § 1.114:
 - (X) Enclosed:
 - (X) Amendment/Reply
 - (X) Return Postcard
- 2. Fees:
 - (X) RCE fee (\$790 large entity)
 - (X) Extension of Time fee:
 - (X) Two Month (\$450)
- 3. Payment:
 - (X) Check in the amount of \$1,240 to cover the above fees.
 - (X) The Commissioner is hereby authorized to charge any additional fees under 37 C.F.R. § 1.16 and § 1.17 which may be required, now or in the future, or credit any overpayment to Deposit Account No. 11-1410.

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Address all future communications to Customer No. 20,995.

Dated: 2/22/05

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